How should the main data bus, mdr, mar, and memory be connected?

How will the memory look?  
Do we need to move blocks around in the memory\*?

Memory LED Features?

All are individually addressable?

Should the memory cells be labeled?

How many writable blocks will we need/what is the maximum number of writable blocks that can be in use at once?

At what height on the memory should the main bus be placed?

*\*if we can write to the memory we could either move the blocks or have preset hardwired display blocks in the memory- i'd guess it would be easier to just have a gantry behind the memory moving the blocks and having one in front of the memory acting as a visual representation of reading and writing to the memory*

Notes

Unsure of the placement of the read write block